

crypto\_kem  
bigquake1  
implementations  
amd64 Bobcat  
amd64 K8  
amd64 K10 65nm  
amd64 K10 45nm  
amd64 K10 32nm  
amd64 Bulldozer  
amd64 Piledriver  
amd64 Zen  
amd64 Zen 2  
amd64 Zen 3  
amd64 Knights Landing  
amd64 Golden Cove  
amd64 Cascade Lake  
amd64 Tiger Lake  
amd64 Skylake+512x2  
amd64 Ice Lake  
amd64 Comet Lake  
amd64 Cannon Lake  
amd64 Coffee Lake  
amd64 Kaby Lake  
amd64 Skylake  
amd64 Broadwell+AES  
amd64 Haswell+AES  
amd64 Ivy Bridge+AES  
amd64 Sandy Bridge+AES  
amd64 Sandy Bridge  
amd64 Westmere  
amd64 Core 2 45nm  
amd64 Core 2 65nm  
amd64 Gracemont  
amd64 Tremont  
amd64 Goldmont Plus  
amd64 Goldmont  
amd64 Airmont  
amd64 Silvermont  
amd64 Bonnell  
ppc32 G3  
riscv64 U54  
mipso32 Ocheon II  
armeabi Armada  
armeabi Cortex-A7  
armeabi Cortex-A8  
armeabi Cortex-A9+NEON  
armeabi Cortex-A15  
aarch64 X-Gen  
aarch64 Cortex-A53  
aarch64 Cortex-A53+crypto  
aarch64 Cortex-A57+crypto  
aarch64 Cortex-A72  
aarch64 Cortex-A72+crypto  
aarch64 ThunderX2  
Time

? :ref

Table listing various processor models and their performance metrics. The table is organized into columns, with the first column listing processor names and the subsequent columns containing numerical performance values. The table is partially obscured by a blue line graph.