<table>
<thead>
<tr>
<th>Processor Type</th>
<th>Model 1</th>
<th>Model 2</th>
<th>Model 3</th>
<th>Model 4</th>
<th>Model 5</th>
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</thead>
<tbody>
<tr>
<td>aarch64 A72</td>
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<tr>
<td>aarch64 Zen2</td>
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<td>aarch64 Zen</td>
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<tr>
<td>aarch64 CascadeLake</td>
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<td>aarch64 SL+512x2</td>
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<td>aarch64 CannonLake</td>
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<td>aarch64 CoffeeLake</td>
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<td>aarch64 KabyLake</td>
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<td>aarch64 Skylake</td>
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<td>aarch64 BW+AES</td>
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<td>aarch64 HW+AES</td>
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<td>aarch64 IB+AES</td>
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<tr>
<td>aarch64 SB+AES</td>
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<tr>
<td>aarch64 Sandy Bridge</td>
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<td>aarch64 Piledriver</td>
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<td>aarch64 Bulldozer</td>
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<td>aarch64 Westmere</td>
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<tr>
<td>aarch64 C2 65nm</td>
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<tr>
<td>aarch64 K10 32nm</td>
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<tr>
<td>aarch64 K10 45nm</td>
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<td>aarch64 K10 65nm</td>
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<tr>
<td>aarch64 Goldmont</td>
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<td>aarch64 K8</td>
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<td>aarch64 Bobcat</td>
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<td>aarch64 Atom</td>
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<td>ppc32 G3</td>
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<td>riscv64 U54</td>
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<td>mips32 Octeon II</td>
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<td>arm64i Armbi Armada</td>
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<td>arm64i Cortex-A7</td>
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<td>arm64i Cortex-A9+NEON</td>
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<td>arm64i Cortex-A15</td>
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<td>aarch64 X-Gene</td>
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<td>aarch64 A53</td>
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<td>aarch64 A53+crypto</td>
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<tr>
<td>aarch64 A57+crypto</td>
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<td>aarch64 A72+crypto</td>
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<td>aarch64 ThunderX2</td>
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**Notes:**
- Models may vary depending on the specific implementation and version.
- Some processors support additional features like crypto acceleration.
- The table is not exhaustive and may require further research for complete accuracy.

**Additional Resources:**
- [Cryptographic Hash Functions](https://bench.cr.yp.to)
- [KCP Optimized1600](https://bench.cr.yp.to)

**Time:**
- 16384
- 65536
- 262144
- 1048576